IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

TZENG Group Art Unit: Not yet assigned

Application No.: New Application Examiner: Not yet assigned

Filed: August 31, 2001 Attorney Dkt. No.: 023925-00015

For: LINKED NETWORK SWITCH CONFIGURATION

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Washington, D.C. 20231

August 31, 2001

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the information item(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each item(s) is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the item(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

\boxtimes	first O	This Information Disclosure Statement is being filed (a) within three months U.S. filing date or the date of filing a CPA, OR (b) before the mailing date of a ffice Action on the merits in the present application, or (c) accompanies a est for Continued Examination. No certification or fee is required.
		This Information Disclosure Statement is being filed more than three months ne U.S. filing date AND after the mailing date of the first Office Action on the , but before the mailing date of a Final Rejection or Notice of Allowance.
		a. I hereby certify that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 CFR §1.97(e)(1).
		b. I hereby certify that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this Information Disclosure Statement. 37 CFR §1.97(e)(2).
		c. A check in the amount of \$180.00 in payment of the fee under 37 CFR §1.17(p). Please charge any fee deficiency or credit any overpayment to Deposit Account No. 01-2300 as needed to ensure consideration of the disclosed information.

	Allowa the Inf amour charge	This Information Disclosure Statement is being filed more than three months he U.S. filing date and after the mailing date of a Final Rejection or Notice of ance, but before payment of the Issue Fee. Applicant(s) hereby petition(s) that formation Disclosure Statement be considered. Attached is our check in the nt of \$130.00 in payment of the petition fee under 37 CFR §1.17(i)(1). Please any fee deficiency or credit any overpayment to Deposit Account No. 01-2300 eded to ensure consideration of the disclosed information.
		a. I hereby certify that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 CFR §1.97(e)(1).
		b. I hereby certify that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this Information Disclosure Statement. 37 CFR §1.97(e)(2).
\boxtimes	4. are att	An English-language Abstract(s) of the non-English language reference(s) ached hereto.
		Respectfully submitted,

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PTO-1449 Form; 65 References Enclosures:

FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

LIST OF REFERENCES CITED BY APPLICANT

(Use several sheets if necessary)

ATTY. DOCKET NO. 023925-00015

SERIAL NO. P

Sheet 1 of 3

APPLICANT

TZENG

FILING DATE
August 31, 2001

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	AA	5,555,398	Sep. 10, 1996	Raman			Apr. 15, 1994
	AB	5,644,784	Jul. 1, 1997	Peek		ļ. <u></u> .	Mar. 3, 1995
	AD	5,828,653	Oct. 27, 1998	Goss			Apr. 26, 1996
	AE	5,423,015	Jun. 6, 1995	Chung			May 21, 1991
	AF	5,696,899	Dec. 9, 1997	Kalwitz			Nov. 18, 1992
	AG	6,061,351	May 9, 2000	Erimli et al.			Dec. 18, 1997
	АН	5,748,631	May 5, 1998	Bergantino et al.			May 9, 1996
	Al	6,119,196	Sep. 12, 2000	Muller et al.			Jun. 30, 1997
	AJ	6,175,902 B1	Jan. 16, 2001	Runaldue et al.			Dec. 18, 1997
	AK	5,473,607	Dec. 5, 1995	Hausman et al.			Aug. 9, 1993
	AL	5,787,084	Jul. 28, 1998	Hoang et al.			Jun. 5, 1996
	АМ	5,845,081	Dec. 1, 1998	Rangarajan et al.			Sep. 3, 1996
	AN	5,892,922	Apr. 6, 1999	Lorenz			Feb. 28, 1997
	AO	5,802,287	Sep. 1, 1998	Rostoker et al.			Aug. 3, 1995
	AP	6,011,795	Jan. 4, 2000	Varghese et al.			Mar. 20, 1997
	AQ	6,185,185 B1	Feb. 6, 2001	Bass et al.			Nov. 21, 1997
	AR	5,524,254	Jun. 4, 1996	Morgan et al.	ļ		Jul. 1, 1994
	AS	5,987,507	Nov. 16, 1999	Creedon et al.			Dec. 29, 1998
	AT	5,831,980	Nov. 3, 1998	Varma et al.			Sep. 13, 1996
	AU	5,781,549	Jul. 14, 1998	Dai		_	Feb. 23, 1996
	AV	6,041,053	Mar. 21, 2000	Douceur et al.			Sep. 18, 1997
	AW	5,459,717	Oct. 17, 1995	Mullan et al.	ļ		Mar. 25, 1994
	AX	5,909,686	Jun. 1, 1999	Muller et al.			Jun. 30, 1997
N=	AY	5,887,187	Mar. 23, 1999	Rostoker et al.			Apr. 14, 1997
	AZ	5,568,477	Oct. 22, 1996	Galand et al.	-		Jun. 27, 1995
	ВА	5,414,704	May 9, 1995	Spinney			Apr. 5, 1994
	вв	5,390,173	Feb. 14, 1995	Spinney et al.	-		Oct. 22, 1992
	вс	5,825,772	Oct. 20, 1998	Dobbins et al.			Apr. 2, 1996
	BD	5,790,539	Aug. 4, 1998	Chao et al.	<u></u>		Jan. 29, 1996

U.S. PATENT DOCUMENTS

U.S. PATENT DOCUMENTS Sheet 2						Sheet 2 of 3	
EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	BE	5,918,074	Jun 29, 1999	Wright et al.			Jul. 25, 1997
	BF	5,898,687	Apr. 27, 1999	Harriman et al.			Jul. 24, 1996
	BG	5,940,596	Aug. 17, 1999	Rajan et al.			Aug. 4, 1997
	вн	5,802,052	Sep. 1, 1998	Venkataraman			Jun. 26, 1996
	ВІ	5,842,038	Nov. 24, 1998	Williams et al.			Oct. 10, 1996
	ВЈ	5,742,613	Apr. 21, 1998	MacDonald			Apr. 27, 1993
	вк	5,579,301	Nov. 26, 1996	Ganson et al.			Feb. 28, 1994
	BL	5,278,789	Jan. 11, 1994	Inoue et al.			Dec. 6, 1991
	вм	5,652,579	Jul. 29, 1997	Yamada et al.			Aug. 15, 1996
	BN	5,499,295	Mar. 12, 1996	Cooper			Aug. 31, 1993

FOREIGN PATENT DOCUMENTS

Γ	1	1	T TOO THE TOO			T		
	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB- CLASS	TR/ YES	NO F	ION PART.
во	WO/9900936	Jan. 7, 1999	WIPO			xx		
ВР	EP 0 465 090 A1	Jan. 8, 1992	EPO			xx		
BQ	EP 0 859 492 A2	Aug. 19, 1998	EPO			xx		
BR	EP 0 312 917 A2	Apr. 26, 1989	EPO			хх		
BS	EP 0 853 441 A2	Jul. 15, 1998	EPO			xx		
ВТ	4-189023	Jul. 7, 1992	Japan					Abs
BU	WO 98/09473	Mar. 5, 1998	WIPO			хх		
BV	EP 0 752 796 A2	Jan. 8, 1997	EPO			хх		
BW	EP 0 862 349 A2	Sep. 2, 1998	EPO					Abs
вх	FR 2 725 573	Apr. 12, 1996	France					Abs
BY	EP 0 854 606 A2	Jul. 22, 1998	EPO			xx		
BZ	WO 99/00948	Jan. 7, 1999	WIPO			xx		
CA	WO 99/00949	Jan. 7, 1999	WIPO			xx		
СВ	WO 99/00950	Jan. 7, 1999	WIPO			xx		
СС	WO 99/00939	Jan. 7, 1999	WIPO			xx		
CD	WO 99/00938	Jan. 7, 1999	WIPO			хх		
CE	WO 99/00944	Jan. 7, 1999	WIPO		,	xx		
CF	WO 99/00945	Jan. 7, 1999	WIPO			xx		
CG	EP 0 849 917 A2	Jun. 24, 1998	EPO			xx		
СН	EP 0 907 300 A2	Apr. 7, 1999	EPO		<u>.</u>	хх		

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	OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.) Sheet 3 of 3					
CI	"A High-Speed CMOS Circuit for 1.2-Gb/s 16 x 16 ATM Switching," Alain Chemarin et al. 8107 IEEE Journal of Solid-State Circuits 27(1992) July, No. 7, New York, US, pages 1116-1120					
cı	"Local Area Network Switch Frame Lookup Technique for Increased Speed and Flexibility," 700 IBM Technical Disclosure Bulletin 38(1995) July, No. 7, Armonk, NY, US, pages 221-222					
СК	"Queue Management for Shared Buffer and Shared Multi-buffer ATM Switches," Yu-Sheng Lin et al., Department of Electronics Engineering & Institute of Electronics, National Chiao Tung University, Hsinchu, Taiwan, R.O.C., March 24, 1996, pages 688-695					
CL	"A 622-Mb/s 8 x 8 ATM Switch Chip Set with Shared Multibuffer Architecture," Harufusa Kondoh et al., 8107 IEEE Journal of Solid-State Circuits 28(1993) July, No. 7, New York, US, pages 808-814					
СМ	"Catalyst 8500 CSR Architecture," White Paper XP-002151999, Cisco Systems Inc. 1998, pages 1-19					
CN	"Computer Networks," A.S. Tanenbaum, PRENTICE-HALL INT., USA, XP-002147300(1998), Sec. 5.2-Sec. 5.3, pages 309-320					

EXAMINER		DATE CONSIDERED			
*EXAMINER:	Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.				